References

[1] N. Ahmed, T. Natarajan, and K. R. Rao, "Discrete Cosine Transform," *IEEE Transactions on Computers*, vol. C-23, pp. 90-94, January 1974.

[2] *ASIC Design Manual*. National Semiconductor Corporation, 2900 Semiconductor Drive, Santa Clara, CA, Rev. 1, pp. 1.5 - 1.7, 1987.

[3] A. DeHon, "Reconfigurable Architectures and General-Purpose Computing in the MOS VLSI Era," Berkeley, CA, pp. 9 and 29, 1996.

[4] D. Denning, *Cryptography and Data Security*, Addison-Wesley Publishing Company, Reading, MA, pp. 90-101, 1982.

[5] M. Gardner, "Mathematical Games: The fantastic combinations of John Conway's new solitaire game 'life'," *Scientific American*, pp. 120-123, October 1970.

[6] M. Gardner, "Mathematical Games: On cellular automata, self-reproduction, the Garden of Eden, and the game 'life'," *Scientific American*, pp. 112-117, February 1971.

[7] M. Gokhale, et. al., "Building and Using a Highly Parallel Programmable Logic Array," *Computer*, vol. 24, no. 1, pp. 81-89, January 1991.

[8] P. Groeneveld and P. Stravers, *OCEAN: The Sea-of-Gates Design System*. On-line user's manual, Electronic Engineering Group, Delft University of Technology, Delft, the Netherlands, 1993[3].

[3]HTML version available at http://olt.et.tudelft.nl/usr1/patrick/public_html/docs/wwman/wwman.html.

[9] D. T. Hoang, "A Systolic Array for Rapid String Comparison", Brown University, Providence, RI, Technical Report CS-92-22, 1992.

[10] E. E. Hollis, *Design of VLSI Gate Array ICs*, Englewood Cliffs, NJ: Prentice-Hall, Inc., 1987.

[11] Li, Weiping, "A New Algorithm to Compute the DCT and its Inverse," *IEEE Transactions on Signal Processing*, June 1991.

[12] D. P. Lopresti, "Rapid Implementation of a Genetic Sequence Comparator Using Field-Programmable Logic Arrays," presented at Advanced Research in VLSI Conference, Santa Cruz, pp. 138-191, March 1991, Invited paper.

[13] C. Mead and L. Conway, *Introduction to VLSI Systems*, Reading, MA: Addison-Wesley, 1980.

[14] D. A. Patterson and J. L .Hennessy, *Computer Organization & Design: The hardware/software interface.* Morgan Kaufmann Publishers, Inc., San Francisco, CA, 1994.

[15] *The Programmable Logic Data Book.* Xilinx, Inc., 2100 Logic Drive, San Jose, CA, pp. 2.5-2.46, 1994.

[16] T. Quarles, et. al., *SPICE3 Version 3f3 User's Manual*, On-line manual, Department of Electrical Engineering and Computer Sciences, University of California, Berkeley, CA, May 1993.

[17] J. Vuillemin, et. al., "Programmable Active Memories: the Coming of Age", Digital Equipment Corporation, Paris Research Laboratory, Rueil-Malmaison Cedex, France, pp. 1-30, July 1994.

[18] N. Weste and K. Eshraghian, *Principles of CMOS VLSI Design*, Reading, MA: Addison-Wesley Publishing Company, 1993.

[19] R. Woods, A. Cassidy, and J. Gray, "VLSI Architectures for Field Programmable Gate Arrays: A Case Study," *IEEE Workshop on FPGAs for Custom Computing Machines*, 1996.